# **Evolvable Systems** for Space Applications





#### Jason D. Lohn

Evolvable Systems Group Computational Sciences Division NASA Ames Research Center Mountain View, CA USA

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- NASA Interest in Evolvable Systems
- Example Applications
  - Self-repairing FPGAs
  - Circuit Design
  - Antenna Design
- Future & Conclusion



# The NASA Mission



To explore the Universe and search for life

To inspire the next generation of explorers







A system that changes over time to become better...

Changing its shape, its function, being automatically designed...

All under the guidance of a process derived from biological evolution.



### **Examples of Evolvable Systems**





Automatic Fault Recovery

Electronics Survive Extreme Radiation, Temperature



Evolved Controller Circuits Enable Mobile Drilling



Robotic Control

Coordination Among Modukar Robotic Structures



Automated Design with New Devices

Defect Tolerant Nanosystems

### **Evolutionary and Adaptive Algorithms**



Abundance of:

- Optimization problems
- Design problems
- Applications requiring automation, fault recovery, control

Agency-wide:

- Many projects using EAs
- Ames & JPL each have groups dedicated to evolvable systems



- Evolutionary Algorithms are beginning to design complex engineering structures at the level of human experts
- John Koza's list of human-competitive performance (many infringe on patents)





## **Evolutionary Design Engine**





# Fault Recovery For Reconfigurable Systems

(1 Digital,1 Analog)



# Fault Recovery





• <u>Description</u>:

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- Fault tolerance / self-repair in extreme environments
  - High temperature
  - High radiation
- <u>Output</u>: adaptive algorithms for autonomous self-repair of reprogrammable logic chips



chip reprogrammed by algorithm

# **Quadrature Decoder**



- Applications requiring determination of angular translation (or speed)
- Example: DC-motor to drive system for a mobile robot we may wish to move forward (or reverse) by a fixed distance
- Decoder determines rotation direction



# **Quadrature Decoder**

• Finite state machine





State transition table

	СНА	СНВ	STATE
	1	0	1
	1	1	2
	0	1	3
Sys	0	0	4



- Logic bits in the LUTs
- Routing bits specify how to connect LUT outputs to LUT inputs



R-CLB = REMOTE CLB R-LUT = REMOTE LUT



### **Results**





**Evolved Quad Decoder Configuration** 



# Hardware Evolution



Hardware Evolution

- 20 random latchup faults are simulated
- Quad decoder is repaired automatically in minutes
- FPGA chip is reprogrammed about 5 times per second we will soon go much faster

Celoxica RC1000 board Xilinx Virtex 1000e





## Hardware Setup





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#### **Programmable Transistor** Array Cell – FPTA2



- TSMC 0.18micron 1.8v; Area 5mmX 7mm; 64 reconfigurable cells; 96 analog in, 64 analog out
- 256 pins; 16bits data bus/9bits address bus; 5000 programming bits
- Each cell OpAmp level





#### Extreme Environment integrating FPTA-2 and SABLES





### **Recovery of Half-wave rectifier at 280C**

- Reconfigurable Cell (FPTA-2)
  - 2 cells: 0,1
  - Input: 1 Analog Input (0.9 Volt amplitude 2 kHz)
- Evolution Mechanism:
  - Goal: Synthesize Half-way rectifier at 280°C.
  - Constrained evolution to Cell0 and Cell1
  - Fitness Function: Absolute error to the target;
- Genetic Algorithm
  - 400 individuals

- Chromosome: 160 bits
- less than 50 generations 1 min





### Recovery of Halfwave Rectifier at 280C





- Input Freq.: 2 kHz
- Input Amplitude: 0.9 Volt

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Low

ami

9 J I

Temperature 280C:

1.00 V

Ch2

Ch1

1.00 V

**Recovery by Evolution** 

04.00.0

M 400µs A Ch1 J 400mV



# Automated Circuit Design





• Template circuit





parts bins

#### Evolvable Systems =



- a small circuit construction language is used
- instructions involve placing components end-to-end (laying a trail)
- examples:
  - place\_resistor(144.92, new\_node)
  - place\_transistor(ground, new\_node, previous\_node)
  - place\_capacitor(183e-6, ground)

### From Representation to **Fitness Calculation**











### **Evolved 85 dB Amplifier**





transistor(N, ACTIVE\_NODE, NEW\_NODE, INPUT\_NODE); transistor(N, BASE, ACTIVE\_NODE, PREVIOUS\_NODE); resistor\_cast\_to\_ps(4.618467e+04); capacitor\_cast\_to\_input(1.628423e-04); transistor(N, NEW\_NODE, ACTIVE\_NODE, GROUND\_NODE); resistor\_cast\_to\_ps(9.396477e+04); transistor(N, NEW\_NODE, ACTIVE\_NODE, GROUND\_NODE); transistor(P, NEW\_NODE, ACTIVE\_NODE, GROUND\_NODE); transistor(P, NEW\_NODE, ACTIVE\_NODE, PS\_NODE); transistor(N, NEW\_NODE, ACTIVE\_NODE, PS\_NODE); transistor(N, PS\_NODE, ACTIVE\_NODE, NEW\_NODE); transistor(N, PS\_NODE, ACTIVE\_NODE, NEW\_NODE);

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# Automated Antenna Design and Optimization

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# **ST5** Mission



- New Millennium Program mission
- Three nanosats
- Measure effect of solar activity on the Earth's magnetosphere







# ST5 Antenna Requirements



- Transmit Frequency: 8470 MHz
- Receive Frequency: 7209.125 MHz
- Antenna RF Input: 1.5W = 1.76 dBW = 31.76 dBm
- VSWR: < 1.2 : 1 at the antenna input port at Transmit Freq, < 1.5 : 1 at the antenna input port at Receive Freq
- Antenna Gain Pattern: Shall be 0 dBic or greater for angles 40 <= theta <=80; 0 <= phi <= 360
- Antenna pattern gain (this shall be obtained with the antenna mounted on the ST5 mock-up) shall be 0.0 dBic (relative to anisotropic circularly polarized reference) for angles 40 <= theta <=80; 0</li>
   <= phi <= 360, where theta and phi are the standard spherical coordinate angles as defined in the IEEE Standard Test Procedures for Antennas, with theta=0 to direction perpendicular to the spacecraft top deck. The antenna gain shall be measured in reference to a right hand circular polarized sense (TBR).
- Desired: 0 dBic for theta = 40, 2 dBic for theta = 80, 4 dBic for theta = 90, for 0 <= phi <= 360
- Antenna Input Impedance: 50 ohms at the antenna input port
- Magnetic dipole moment: < 60 mA-cm^2
- Grounding: Cable shields of all coaxial inputs and outputs shall be returned to RF ground at the transponder system chasis. The cases of all comm units will be electrically isolated from the mounting surface to prohibit current flow to the spacecraft baseplate.
- Antenna Size: diameter: < 15.24 cm (6 inches), height: < 15.24 cm (6 inches)
- Antenna Mass: < 165 g

# **ST5 Quadrifilar Helical Antenna**





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# Antenna Genotype

- Genotype specifies design of 1 arm in 3-space
- Genotype is tree-structured computer program that builds a wire form
- Commands:

Fvnlvahle

- forward(length radius)
- rotate\_x(angle)
- rotate\_y(angle)
- rotate\_z(angle)
- Branching in genotype → branching in wire form







- Forward commands can have 0,1,2, or 3 children
- Rotate commands have exactly 1 child (always nonterminal)





# **EA Parameters**



- Generational
- Rank-based selection
- Elitism of 2
- Population=200 (seeded w/individuals from previous run)
- Crossover: 1pt, 50%
- Mutation: 50%:
  - Mutate a constant,
  - Mutate a function (terminals remain terminals),

or

- Add/delete node (for adds: terminals remain terminals)
- Crossover, mutation mutually exclusive



# **Fitness Function**

- Antenna designs are evaluated by NEC4 running on Linux Beowulf supercomputer
- 3 copies w/added noise are evaluated for each design
- Fitness function (to be minimized):
   F = VSWR\_Score \* Gain\_Score \* Penalty\_Score
- VSWRs from both freqs are scaled and multiplied



VSWR

$$f = 0 if gain > 0.5 dB$$
  
$$f = 0.5 - gain if gain < 0.5 dB$$

Penalty: proportional to # gain samples less than 0.01 dB



# Fitness Function: VSWR

voltage standing wave ratio: quantifies reflected-wave interference
the ratio between the highest voltage and the lowest voltage in the signal envelope along a transmission line

 $v_r = VSWR$  at receive frequency  $v'_{r} = \begin{cases} v_{r} + 2.0(v_{r} - 1.25) & \text{if } v_{r} > 1.25 \\ v_{r} & \text{if } 1.25 > v_{r} > 1.1 \\ 1.1 & \text{if } v_{r} < 1.1 \end{cases}$ if  $v_r < 1.1$  $v_t = VSWR$  at transmit frequency  $v'_t = \begin{cases} v_t + 2.0(v_t - 1.15) & \text{if } v_t > 1.15 \\ v_t & \text{if } 1.15 > v_t > 1.1 \\ 1.1 & \text{if } v_t < 1.1 \end{cases}$  $vswr = v'_r v'_t$ 



# **Fitness Function: Gain**

$$\begin{array}{lll} gain_{ij} &=& \mathrm{gain} \ \mathrm{at} \ \theta = 5^{\circ}i, \ \phi = 5^{\circ}j \\ gain(i,j) &=& \left\{ \begin{array}{ll} 0 & \mathrm{if} \ gain_{ij} > 0.5 \\ 0.5 - gain_{ij} & \mathrm{if} \ gain_{ij} < 0.5 \end{array} \right. \\ gain &=& 1 + 0.1 \sum_{i=8}^{i < 19} \sum_{j=0}^{j=72} gain(i,j) \end{array}$$

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## **Evolved Antennas**





ST5-3-10

ST5-4W-03

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## Antenna for ST5 Mission





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# VSWR for Antenna ST5-3-10



Data measured at GSFC on prototype ST5-3-10, Build LIR-2003-02-08



## Gain for Antenna ST5-3-10





8.47 GHz Compact range

Data measured at GSFC on prototype ST5-3-10, Build LIR-2003-02-08

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### Prediction vs Measurement for Antenna ST5-3-10





#### Phi = 0 degrees NEC4 Simulator Data measured at GSFC on prototype ST5-3-10, Build LIR-2003-02-08

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### Comparison: Pattern Analysis for Antennas @ 7.2 GHz



### **Evolved Antenna**

### **Conventional Antenna**



Shaded Yellow Box Denotes Area In-Spec, According to Original Mission Requirements

Ames Research Center

### Comparison: Pattern Analysis for Antennas @ 8.47 GHz



### **Evolved** Antenna

### **Conventional Antenna**



Shaded Yellow Box Denotes Area In-Spec, According to Original Mission Requirements

#### Evolvable Systems

# **Directionality Comparison**



Evolved Antenna (red) vs QHA (green) Data is normalized to 0 dBic (RHCP) for each antenna -3 dB for each division

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# **Evolved Antenna Animation**





- genetic representation
- genotype  $\rightarrow$  phenotype mapping
- position on satellite







- Potential Lower Power: evolved antenna achieves high gain (2-4dB) across a wider range of elevation angles
  - allows a broader range of angles over which maximum data throughput can be achieved
  - may require less power from the solar array and batteries
- More Uniform Coverage: Very uniform pattern with small ripples in the elevations of greatest interest (40 – 80 degrees)
  - allows for reliable performance as elevation angle relative to the ground changes
- Inexpensive Design Cycle: 3 person-months to run algorithms and fabricate the first prototype as compared to 5 person-months for contractor team
- On Schedule to be First Evolved Hardware in Space when mission launches in 2004



# **Future & Conclusion**







- Automated design across multiple levels: macro, micro, nano
- New antenna design problems
- Evolved nano-devices and architectures
- FPGA Self-Repair (real-time/subsecond repair)
- MEMS design & manufacturing problems
- Ornithopters (macro/micro)
- Modular robotic control





- Evolutionary design/optimization:
  - Fast design cycles save time/money
  - Fast design cycles allow iterative "what-if"
  - May discover designs that challenge expert designers
- Which NASA applications area a good fit for EAs?
- Evolved antenna for ST5:

- Meets mission requirements
- Unusual organic topology
- Currently undergoing space qualification